

### FEATURES

- J-STD-020D reflow
- RoHS compliant
- Compatible with leading chip sets
- 2kVrms isolation
- Industry-standard pinout
- Surface mount option
- UL 94 V-0 package materials
- Low profile
- Suitable for both 75 & 110Ω circuits
- Toroidal construction
- Compliant with AES/EBU standards

### DESCRIPTION

The Digital Audio Range of transformers is designed to improve the balance of transmitter and receiver circuitry in hi-fi equipment, video games and other applications requiring high-performance digital audio transmission. Compliant with AES/EBU recommendations for the digital audio interface, offering optimised shunt capacitance between primary and secondary windings. Capable of operating over the audio data rate frequency range, providing isolation from 50-60Hz noise.

For through hole versions please refer to the DA100 series datasheet.

### SELECTION GUIDE

| Order Code <sup>1</sup> | Turns Ratio | Primary Inductance<br>(10kHz, 50mV) | Max. Leakage Inductance<br>(100kHz, 10mV) | Min. Volt-time Product, Et | Min. Return Loss<br>(100kHz-3MHz) | Typ. Common Mode Rejection<br>(100kHz, 110Ω) |
|-------------------------|-------------|-------------------------------------|---|----------------------------|-----------------------------------|--|
|                         |             | mH                                  | μH  | Vμs                        | dB                                |  |
| DA101JC                 | 1:1         | 1.00 - 2.20                         | 0.36                                      | 15                         | 34.6                              | 55.9   |
| DA102JC                 | 1:1         | 2.00 - 4.40                         | 0.65                                      | 20                         | 38.8                              | 52.4   |
| DA103JC                 | 1:1         | 3.20 - 8.50                         | 0.91                                      | 28                         | 34.9                              | 48.5   |

### ISOLATION CHARACTERISTICS

| Parameter         | Conditions                | Min. | Typ. | Max. | Units |
|-------------------|---------------------------|------|------|------|-------|
| Isolation voltage | Flash tested for 1 second | 2000 |      |      | Vrms  |

### TEMPERATURE CHARACTERISTICS

|                                      |                 |
|--------------------------------------|-----------------|
| Operating free air temperature range | -40°C to +85°C  |
| Storage temperature range            | -40°C to +125°C |

### TECHNICAL NOTES

#### ISOLATION VOLTAGE

'Hi Pot Test', 'Flash Tested', 'Withstand Voltage', 'Proof Voltage', 'Dielectric Withstand Voltage' & 'Isolation Test Voltage' are all terms that relate to the same thing, a test voltage, applied for a specified time, across a component designed to provide electrical isolation, to verify the integrity of that isolation.

All products in this series are 100% production tested at their stated isolation voltage.

A question commonly asked is, "What is the continuous voltage that can be applied across the part in normal operation?"

For a part holding no specific agency approvals both input and output should normally be maintained within SELV limits i.e. less than 42.4V peak, or 60VDC. The isolation test voltage represents a measure of immunity to transient voltages and the part should never be used as an element of a safety isolation system. The part could be expected to function correctly with several hundred volts offset applied continuously across the isolation barrier; but then the circuitry on both sides of the barrier must be regarded as operating at an unsafe voltage and further isolation/insulation systems must form a barrier between these circuits and any user-accessible circuitry according to safety standard requirements.

#### REPEATED HIGH-VOLTAGE ISOLATION TESTING

It is well known that repeated high-voltage isolation testing of a barrier component can actually degrade isolation capability, to a lesser or greater degree depending on materials, construction and environment. This series has toroidal isolation transformers, with no additional insulation between primary and secondary windings of enamelled wire. While parts can be expected to withstand several times the stated test voltage, the isolation capability does depend on the wire insulation. Any material, including this enamel (typically polyurethane) is susceptible to eventual chemical degradation when subject to very high applied voltages thus implying that the number of tests should be strictly limited. We therefore strongly advise against repeated high voltage isolation testing, but if it is absolutely required, that the voltage be reduced by 20% from specified test voltage.

This consideration equally applies to agency recognised parts rated for better than functional isolation where the wire enamel insulation is always supplemented by a further insulation system of physical spacing or barriers.



For full details go to  
<https://www.murata.com/en-global/products/power/rohs>

1. Components are supplied in both tube and tape and reel packaging, please refer to package specification section. Orderable part numbers are DAXXXJC (50 pieces per tube) and DAXXXJC-R (500 pieces per reel).

All specifications typical at T<sub>A</sub>=25°C unless otherwise specified.

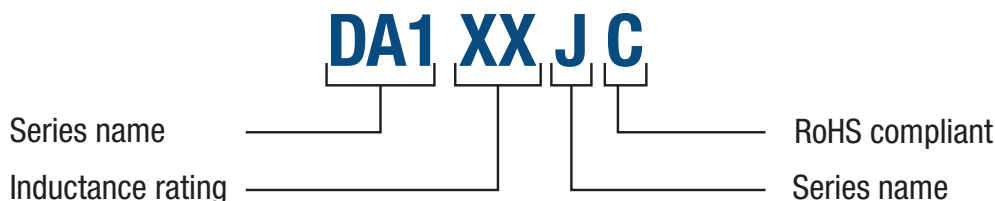
## RoHS COMPLIANCE AND WAVE SOLDERING INFORMATION



This series is compatible with Pb-Free soldering systems and is also backward compatible with Sn/Pb soldering systems. The DA100J series has a process, moisture, and reflow sensitivity classification of MSL1 PSL R7F as defined in J-STD-020 and J-STD-075. This translates to: MSL1 = unlimited floor life, PSL R7F = Peak reflow temperature 245°C with a limitation on the time above liquidus (217°C) which for this series is 100 sec max. The pin termination finish on this product series is Matte Tin.

For further information, please visit [www.murata.com/en-global/products/power/rohs](http://www.murata.com/en-global/products/power/rohs)

## PART NUMBER STRUCTURE



## APPLICATION NOTES

### DA100 series digital audio transformers

The Digital Audio Range of transformers is designed to improve the balance of transmitter and receiver circuitry in hi-fi equipment, video games and other applications requiring high-performance digital audio transmission.

Compliant with AES/EBU recommendations for the digital audio interface, offering optimised shunt capacitance between primary and secondary windings. Capable of operating over the digital audio data rate frequency range (to 6MHz) while providing isolation from 50-60Hz noise.

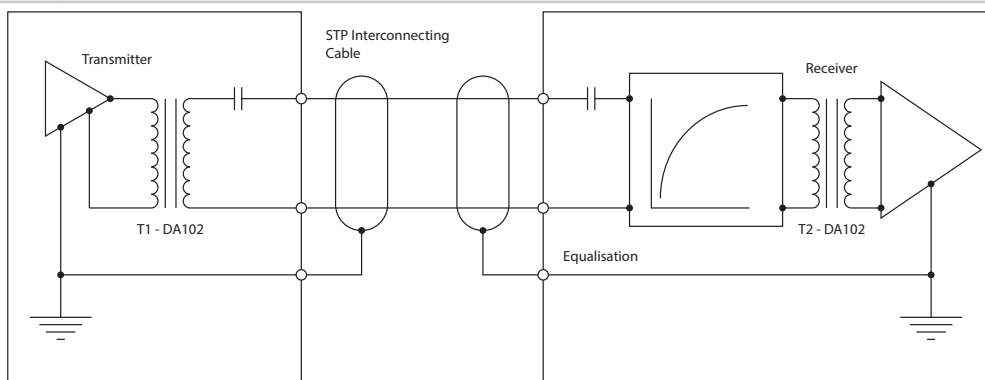
The transformers are 1:1 ratio to reflect the Transmitter Impedance (110Ω) to Receiver Impedance (75Ω) at the connections whether BNC, co-axial or XLR type connectors.

### Digital audio standard

The S/PDIF (Sony/Philips Digital Interface Format) was designed to permit high quality digital stereo audio to be transmitted from a source to a receiver (usually an amplifier or other sound processing system) with no loss in quality of the source signal, in stereo using a multiplexed single transmission line. This was adopted as an International Standard (IEC958) and used extensively in high end audio for home and theatre use, including gaming and high-definition audio interfaces embedded within other communications systems (Figure 1).

An optical version was developed by Toshiba (TosLink/EIAJ optical) that utilises optical fibre connections, but comes at a premium cost to the interface, hence S/PDIF still survives in many applications. Bi-direction transmission is possible, although less common, by multiplexing a transmit and receive signal onto the same cable.

Figure 1: Typical IEC958 digital audio transmission system

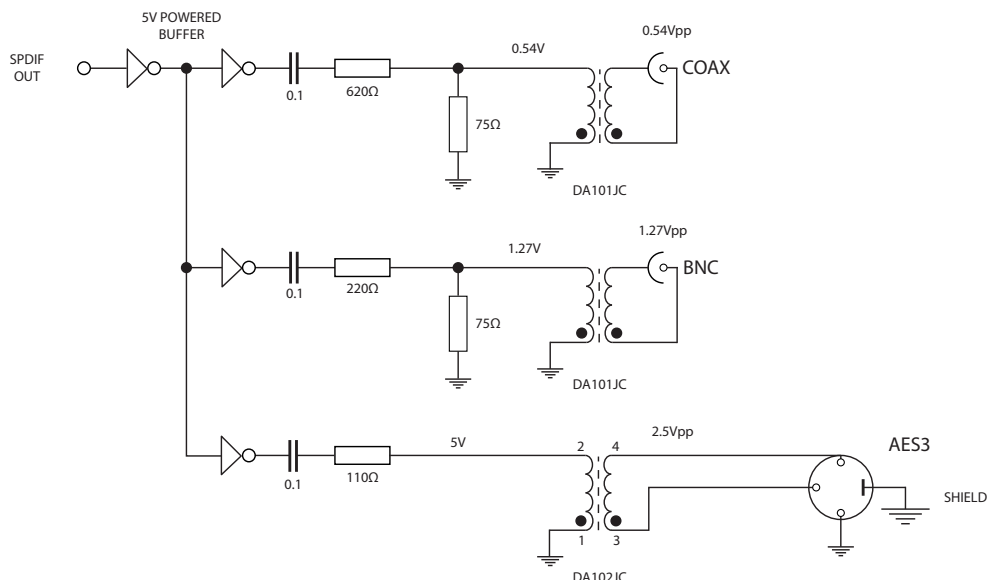


### Different connector options

As noted above, different connectors can be used for the S/PDIF signal, providing the appropriate matching impedance and signalling voltage is maintained. This can be simply implemented by using biasing resistors, the values of which will depend on the supply and output voltage of the S/PDIF buffers (Figure 2).

## APPLICATION NOTES (Continued)

Figure 2: S/PDIF buffered outputs to co-axial, BNC and AES3/XLR connectors

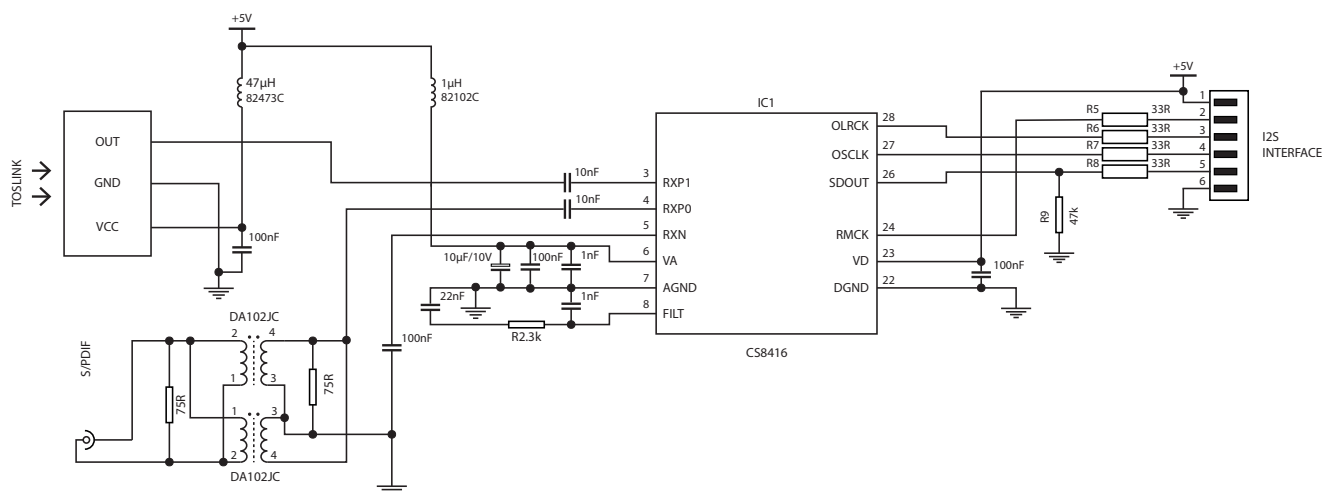


### Other interfaces

Inter-Integrated Circuit Sound (I2S) and TosLink can be interfaced with S/PDIF via an appropriate audio transceiver IC. This gives multiple options for the exchange of digital audio data within a sound processing system (Figure 3).

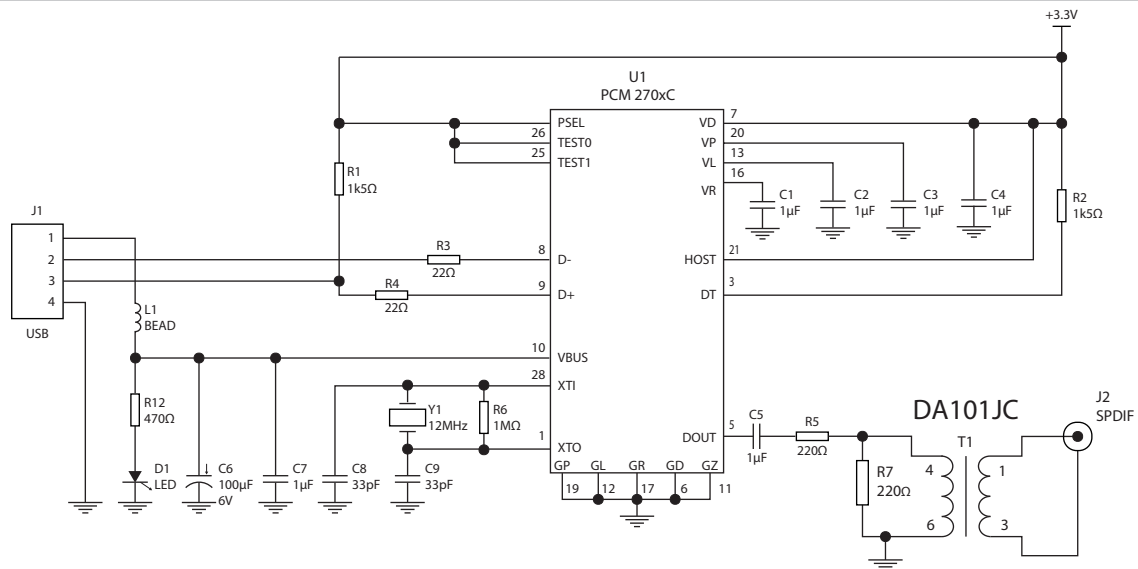
The Universal Serial Bus (USB) standard has taken over a lot of other proprietary digital interface standard in more recent times and this too can be simply interfaced to the S/PDIF signal and isolated to ensure ground noise is minimised between systems (Figure 4).

Figure 3: S/PDIF, TosLink and I2S Interfaces



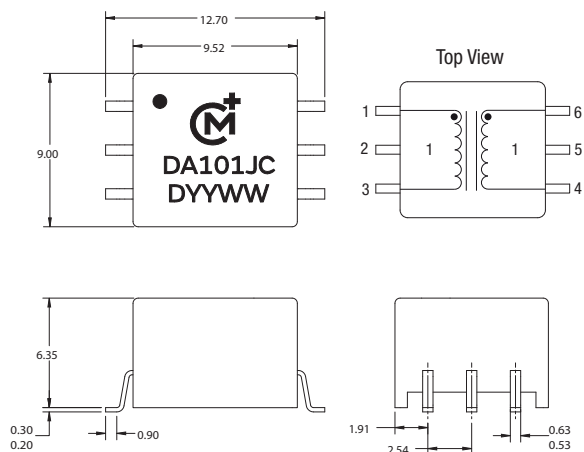
## APPLICATION NOTES (Continued)

Figure 4: S/PDIF to USB Interface



## PACKAGE SPECIFICATIONS

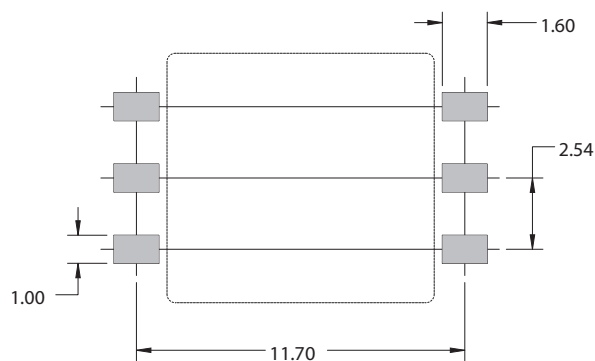
### MECHANICAL DIMENSIONS



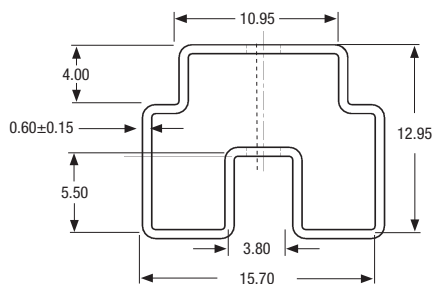
Unless otherwise stated all dimensions in mm  $\pm 0.25$ .  
All pins on a 2.54 pitch and within  $\pm 0.25$  of true position

Package Weight 0.93g Typ.

### RECOMMENDED FOOTPRINT DETAILS



### TUBE OUTLINE DIMENSIONS

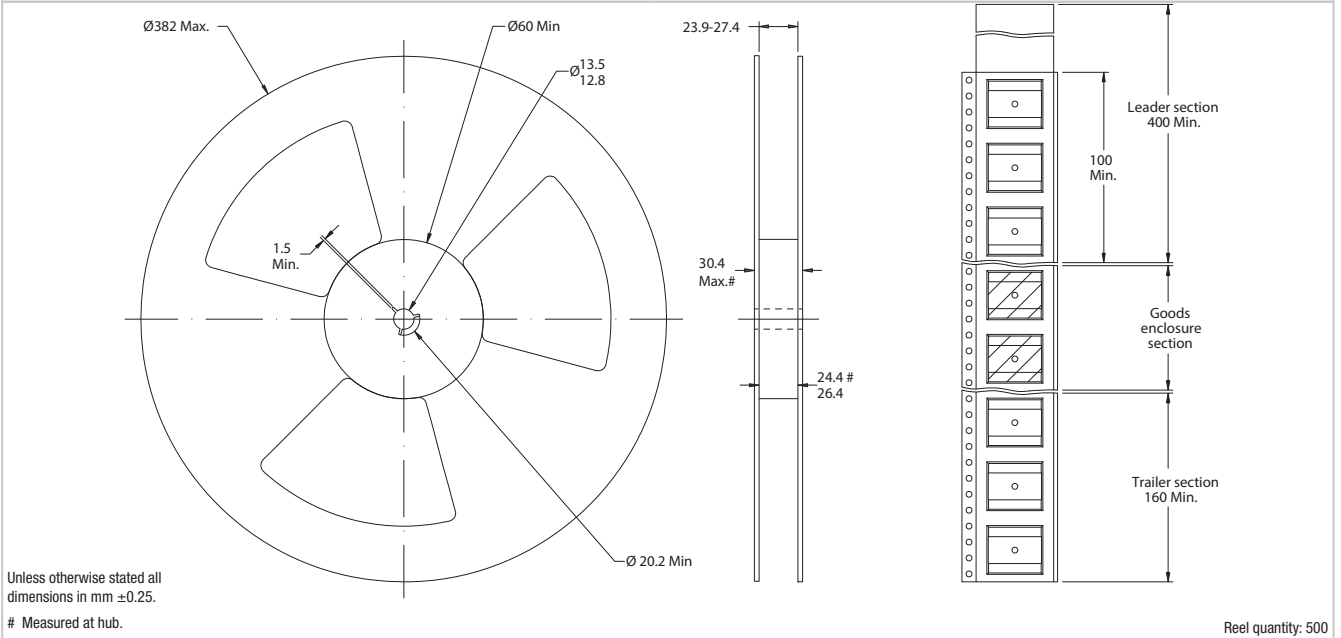


Tube length: 465  $\pm$ 2.  
Tube material: Antistatic coated clear PVC.

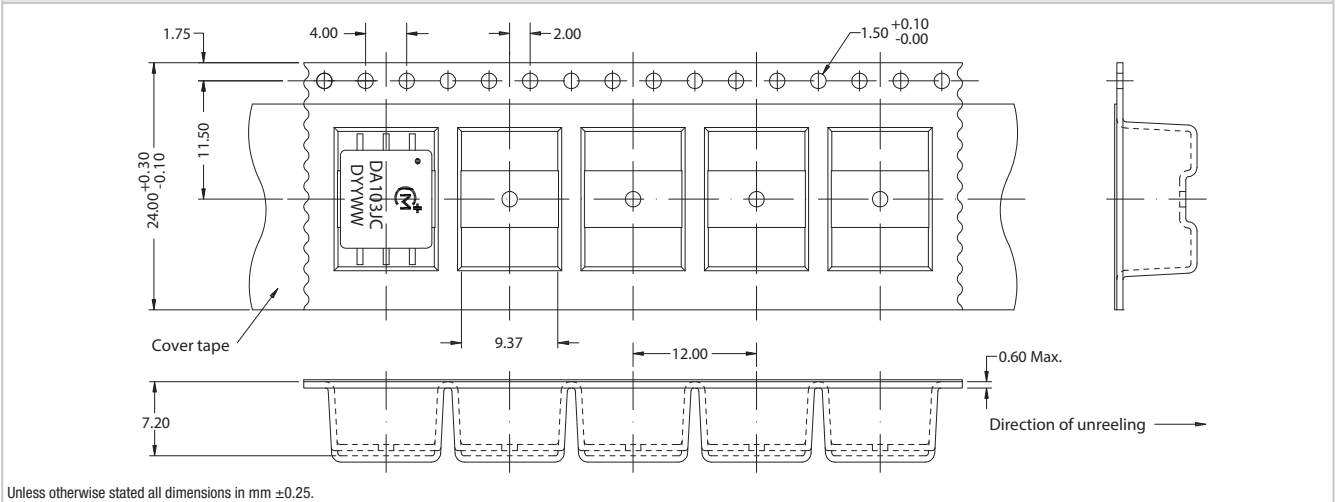
Tube quantity: 50

## TAPE & REEL SPECIFICATIONS

### REEL OUTLINE DIMENSIONS



### TAPE OUTLINE DIMENSIONS



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- Traffic signal equipment
- Disaster prevention / crime prevention equipment
- Data Processing equipment

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